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	Application No.	Applicant(s)		
•	10/648,283	KITAZAWA ET AL.		
Notice of Allowability	Examiner	Art Unit		
·	Michael Rutland-Wallis	2836		
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85; NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.315	ears on the cover sheet with the of (OR REMAINS) CLOSED in this ap) or other appropriate communicatio	correspondence address oplication. If not included n will be mailed in due course. THIS		
1. This communication is responsive to <u>9/18/2007</u> .				
2. The allowed claim(s) is/are <u>1, 5-15 and 18-23</u> .				
 3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do 	e been received. e been received in Application No			
 Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). 				
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	MENT of this application.			
INFORMAL PATENT APPLICATION (PTO-152) which giv				
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.				
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached				
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		•		
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date				
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on the draw the header according to 37 CFR 1.121	ings in the front (not the back) of (d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.				
Attachment(s)	E - N-8 (1-1	Datast Amelication		
1. Notice of References Cited (PTO-892)	5. Notice of Informal I	• •		
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary Paper No./Mail Da			
3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 08/03, 09/05, 12/05	7. X Examiner's Amend			
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. X Examiner's Statem	ent of Reasons for Allowance		
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DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

This application is in condition for allowance except for the presence of claims 16 and 17 directed to a switching module with data decoder non-elected without traverse.

Accordingly, claims 16 and 17 have been cancelled.

Response to Arguments

Applicant's arguments, filed 09/18/2007, have been fully considered and are persuasive. The previous rejections have been withdrawn.

Allowable Subject Matter

Claims 1, 5-15 and 18-21 are allowed. The following is an examiner's statement / of reasons for allowance:

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With respect to claims 1 and 18-21 Bialo teaches the switching circuit of claim 1 however does not teach the control bias supply circuit comprises a diode having an anode to which the voltage signal is applied and a cathode via which the control bias is output to the connection node. At least this further limitation to claim 1 is not taught or rendered obvious by the prior art of record.

With respect to claims 5-15 Bialo teaches the switching circuit of claim 1 however does not teach control bias supply circuit comprises a bias transistor including a structure of a MESFET; and the MESFET having a gate receiving a voltage signal, a first terminal connected to a given potential via a capacitive element, and a second terminal connected to the connection node, the control bias being supplied to the connection node from the second terminal. At least this further limitation to claim 1 is not taught or rendered obvious by the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Rutland-Wallis whose telephone number is 571-272-5921. The examiner can normally be reached on Monday-Thursday 7:30AM-6:00PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

MRW